Agenda

Advanced Power Electronics Interfaces for DE Workshop

Thursday, August 24, 2006
Sponsor: California Energy Commission
Location: 1516 9th Street Sacramento, CA

A WORKSHOP TO FOCUS ON THE STATUS OF DEVELOPMENT OF ADVANCED POWER ELECTRONIC INTERFACES FOR DISTRIBUTED ENERGY APPLICATIONS AND A DISCUSSION OF MODULAR POWER ELECTRONICS, COMPONENT MANUFACTURING, AND POWER ELECTRONIC APPLICATIONS

8:00 - 8:10 8:10 - 8:25	Welcome and Introductory Remarks, <i>Mark Rawson, California Energy Commission</i> Power Electronics - A DOE Perspective, <i>Bill Parks, DOE (Invited)</i>
8:25 - 8:40	Power Electronics Research Assessment, Forrest Small, Navigant
Session 1: Ex	perience with Modular Power Electronics - Chair: Bernard Trenton, CEC
	EXPERIENCE WITH MODULAR POWER ELECTRONICS FROM THE POWER ELECTRONICS BUILDING BLOCK PROGRAM, INTEGRATED POWER ELECTRONICS MODULES AND BRICKS AND BUSES APPROACH
8:40 - 9:00	PEBB Program Experience, Terry Ericsen, Office of Naval Research
9:00 - 9:20	Modular Power Electronics, Fred Wang, Virginia Tech
9:20 - 9:40	Bricks and Buses, Giri Venkataramanan, University of Wisconsin
9:40 - 10:00	General Discussion
10:00 - 10:30	BREAK
Session 2: Ac	Ivanced Concepts and Components - Chair: Dick DeBlasio, NREL
	ADVANCED CONCEPTS FOR POWER ELECTRONICS CONVERTER DESIGNS INCLUDING MATRIX AND MULTI-PORT CONVERTER, MANUFACTURING POWER ELECTRONIC COMPONENTS AND ADVANCED FUNCTIONALITY FOR DISTRIBUTED ENERGY APPLICATIONS.
10:30 - 10:50	Advanced Topologies, Ned Mohan, University of Minnesota
10:50 - 11:10	Integrated Modules Simplify Systems Design, John Mookken, SEMIKRON Inc.
11:10 - 11:30	Power Electronic Interfaces, Ben Kroposki, NREL
11.30 - 12.00	General Discussion
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